

Block Encodings of Discrete Subgroups on Quantum Computer

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We introduce a block encoding method for mapping discrete subgroups to qubits on a quantum computer. This method is applicable to general discrete groups, including crystal-like subgroups such as \mathbb{B} of $SU(2)$ and \mathbb{V} of $SU(3)$. We detail the construction of primitive gates – the inversion gate, the group multiplication gate, the trace gate, and the group Fourier gate – utilizing this encoding method for \mathbb{BT} and for the first time \mathbb{B} group. We also provide resource estimations to extract the gluon viscosity. The inversion gates for \mathbb{BT} and \mathbb{B} are benchmarked on the **Baiwang** quantum computer with estimated fidelities of $40_{-4}^{+5}\%$ and $4_{-3}^{+5}\%$ respectively.

I. INTRODUCTION

Gauge symmetries play important roles in quantum field theories, with the $SU(3) \times SU(2) \times U(1)$ gauge symmetry being particularly important as it encapsulates the interactions in the well-established Standard Model for particle physics. Accurate predictions in the strongly-coupled regime of these interactions require substantial computational resources. Over the past few decades, Monte Carlo methods in lattice gauge theory (LGT) have flourished, benefiting from advances in supercomputing capabilities and algorithmic innovations. Nevertheless, challenges persist, particularly in scenarios involving dynamic processes such as transport coefficients of the quark-gluon plasma [1–3], parton physics in hadron collisions [4–9], and out-of-equilibrium evolution in the early universe [10–13] due to sign problems from the complex-valued nature of the Boltzmann sampling weight. Quantum computers offer a promising avenue to circumvent this challenge by enabling real-time simulations within the Hamiltonian formalism [14–18].

The Hilbert space of LGT is infinite-dimensional, requiring digitization methods to facilitate its mapping onto a finite quantum memory (see Sec VI.b of [17] for different digitization methods). These include the loop-string-hadron (LSH) formulations [19–21] where the Hilbert space is built from gauge-invariant operators, digitizations of independent Wilson loops [22–27], qubitization formulations of gauge theories [28–30], and the focus of this paper – the discrete subgroup approximation [31, 32]. Understanding and reducing the theoretical errors in the digitization is an area of active research [33–39].

TABLE I. Freezing couplings as a function of spatial dimension d , β_f^{d+1} , for crystal-like subgroups of $SU(2)$. For comparison, $\beta_s^{2+1d} = 3$ and $\beta_s^{3+1d} = 2.2$.

G	β_f^{2+1d}	β_f^{3+1d}
\mathbb{BT}^a	3.45(5)	2.25(5)
\mathbb{BO}^b	5.45(5)	3.25(5)
\mathbb{B}	9.65(5)	5.85(5)

^a Numerical results from [45]

^b Numerical results from [53]

LGT calculations are performed at lattice spacing $a = a(\beta)$ which approaches zero for asymptotically free theories as the coupling parameter $\beta \rightarrow \infty$. To perform extrapolation to the continuous spacetime limit, calculations need to be done in the scaling regime with $\beta > \beta_s$. For the discrete subgroup approximation, gauge links will become “frozen” to the identity when $\beta > \beta_f$, leading to different behaviors from the continuous groups, which makes the discrete subgroup approximations to be only valid in the regime $\beta < \beta_f$. Thus to extrapolate to the correct continuous spacetime limit as the continuous group, the discrete subgroup approximations need to satisfy the condition $\beta_f > \beta_s$. The discrete subgroup approximation has been explored for the Abelian group $U(1)$ [40, 41] and $SU(N)$ gauge theories [35, 36, 42–49], including fermions [50, 51]. For the case of $SU(2)$ which we consider in this work, there are three crystal-like subgroups: \mathbb{BT} , \mathbb{BO} , and \mathbb{B} with β_f^d shown in Tab. I. The smallest group, \mathbb{BT} has $\beta_f^{3+1} < \beta_s^{3+1} \sim 2.2$, while the other two subgroups \mathbb{B} , $\beta_f^d \gg \beta_s^d$ as shown in Fig. 1 for both $2+1d$ and $3+1d$ theories. The larger β_f of \mathbb{B} implies that digitization errors should be smaller in comparison to \mathbb{BT} and \mathbb{BO} . Other attempts to increase β_f with modified actions are also being discussed [52].

Though reducing the uncertainties in the extrapolation

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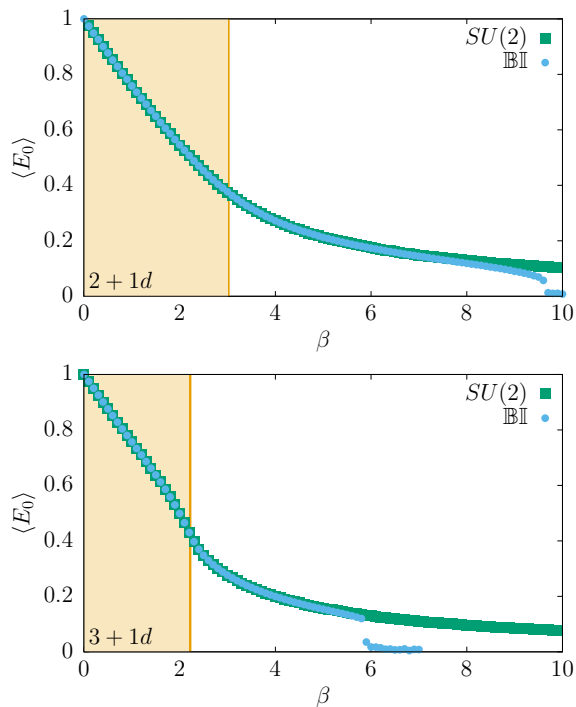


FIG. 1. Average plaquette or lattice energy density $\langle E_0 \rangle$ of $\mathbb{B}\mathbb{I}$ as a function of Wilson coupling parameter β on 8^d lattices for (top) $2+1d$ with $\beta_f^{2+1} = 9.65(5)$ and (bottom) $3+1d$ with $\beta_f^{3+1} = 5.85(5)$. The shaded region corresponds to the phase where $\beta \leq \beta_s$.

to the continuum with larger β_f , quantum simulations of larger discrete group will be more complicated. To simulate the discrete subgroup theory on a quantum computer, one need to obtain the mapping between the elements of the discrete group and the qubits. The usual way is to write the elements of certain discrete subgroup as an ordered product of group generators with the exponents mapped to qubits or qudits on quantum computer [45, 53–55]. However, for the largest crystal-like $\mathbb{B}\mathbb{I}$ and \mathbb{V} subgroups of $SU(2)$ and $SU(3)$ theories, the ordered product expression is unknown yet, and may not be possible. This potential obstacle motivates the search for other encodings of discrete groups.

In this work, we consider a new encoding – *block encoding* – expressing groups as d -dimensional matrices over a finite field \mathbf{F} . According to Cayley’s theorem, any finite group of order n can be mapped by an injective homomorphism to the general linear group $GL(d, \mathbf{F})$ over certain finite field \mathbf{F} , with dimension $d \leq n$. Each matrix element will be sorted in its own register, and then the register representing the group element is built from them. This encoding methods can be applied to generic discrete subgroups, including the largest discrete crystal-like $\mathbb{B}\mathbb{I}$ subgroup of $SU(2)$ and \mathbb{V} of $SU(3)$. After reviewing the basics of group representation in Sec. II, we introduce the block encoding methods. In Sec. III, we review the primary gates to implement group element operations and

basic logic quantum gates set in constructing the primary gates. We then present the construction of primary gates based on the block encoding for the $\mathbb{B}\mathbb{T}$, $\mathbb{B}\mathbb{I}$ in Sec. IV. Resource requirements are estimated and compared to other digitization methods in Sec. V. Fidelities for the block encoding with quantum errors are analyzed in Sec. VI, followed by the benchmarking of the inverse gates in Sec. VII. In Sec. VIII, we present the conclusion and outlook. Appendices include an alternative constructions based on two’s complement for block encoding.

II. DISCRETE SUBGROUP ENCODINGS

For any encoding of discrete groups, the group elements g are mapped to at least as many integers as $p = [0, |G|-1]$ where $|G|$ is the group dimension. Previous work focused on the *ordered product* encoding which maps onto the integers the integer exponents $\{o_k\}$ of group generators $\{\lambda_k\}$ in a particular ordering:

$$g_{\{o_k\}} = \prod_k \lambda_k^{o_k} \quad (1)$$

where $0 \leq o_k < O_k$. O_k can be as large as the generator’s order $\lambda_k^{O_k} = \mathbb{1}$ but often lower when redundancies occur e.g. $\lambda_1^{O_1} = \lambda_2$. An integer mapping is then defined as

$$p = o_k + O_k(o_{k-1} + O_{k-1}(\dots + O_2(o_1 + O_1 o_0))). \quad (2)$$

With this, one may consider encoding p onto quantum memory by decomposing p via Eq. (2) where $\{O_k\}$ are replaced by the dimensionality of the qudits $\{D_k\}$ available. Both $\mathbb{B}\mathbb{T}$ and $\mathbb{B}\mathbb{O}$ have been formulated in this way [45, 53]. As an example, the group elements of $\mathbb{B}\mathbb{T}$ can be represented as

$$g = (-1)^m \mathbf{i}^n \mathbf{j}^o \mathbf{l}^p. \quad (3)$$

where $\mathbf{i}, \mathbf{j}, \mathbf{k}$ are the unit quaternions and $\mathbf{l} = -(\mathbf{1} + \mathbf{i} + \mathbf{j} + \mathbf{k})/2$. m, n, o are 3 binary variables, while p is tertiary, and thus can be encoded for example in a ($p = 8$) quoctit and ($p = 3$) qutrit or in 5 qubits with some forbidden states. A similar construction for $\mathbb{B}\mathbb{O}$ could be realized with 6 qubits, or a qutrit with either 2 quoctits ($p = 4$) or a qudecasexit ($p = 16$) [54]. The corresponding qubit primitive gates to implement group inverse, product, trace and Fourier transformation has been worked out recently [45, 53]. While this encoding can be efficient in quantum memory, it is not a given that all finite groups can be encoding with ordered products.

Due to the potential limitations of the ordered product encoding, we consider a different encoding, block encoding, which instead maps the finite fields in the matrices representing g to a set of qubits. This encoding relies on the ability to represent the matrix elements themselves as valued only over a finite field. Here, we will consider how to encode $\mathbb{B}\mathbb{T}$ and $\mathbb{B}\mathbb{I}$ as examples.

The $\mathbb{B}\mathbb{T}$ group is isomorphic to the special linear group $SL(2, 3)$ — the group of all 2×2 matrices with unit

determinant over the three-element finite field \mathbf{F}_3 (which can be defined as the ring of integers modulo 3). The character table for $\mathbb{B}\mathbb{T}$ is found at the bottom of Tab. II. Using the isomorphism, g can be represented by

$$g \in \left\{ \begin{pmatrix} a & b \\ c & d \end{pmatrix} \mid a, b, c, d \in \mathbf{F}_3, ad - bc \equiv 1 \pmod{3} \right\} \quad (4)$$

This leads to another way of encoding $\mathbb{B}\mathbb{T}$ group elements using four ternary variables $a, b, c, d = \{0, 1, 2\}$ as $|g\rangle = |abcd\rangle$ with a Hilbert space of $3^4 - 57 = 24$ states where 57 states are removed by the determinant constraints.

In this work, we encode each matrix element in Eq. (4) as a binary integer into two qubits, e.g. the non-negative encoding with $|0\rangle = |00\rangle$, $|1\rangle = |01\rangle$, $|2\rangle = |10\rangle$ and a forbidden state $|11\rangle$. Given the redundancy in this encoding, we can also use the two's complement encoding as $|0\rangle = |00\rangle$, $|1\rangle = |01\rangle$ and $|2\rangle = |11\rangle$. In the $\mathbb{B}\mathbb{T}$ case, the two's component encoding is the same as the Gray code [56–59]. Alternatively, the matrix elements could each be encoding in a ($p = 3$) qutrit without forbidden states. In this paper, we adopt the non-negative encoding, resulting in a 8-qubit group register while leaving optimizations of quantum resources among different encoding to the future as considerations to noise is crucial [54].

The block encoding can be extended to other groups. The \mathbb{B} group is isomorphic to the special linear group $SL(2, 5)$ — the group of all 2×2 matrices over the finite field \mathbf{F}_5 with unit determinant. The character table for \mathbb{B} is found at the bottom of Tab. II and the group element can be represented by

$$g \in \left\{ \begin{pmatrix} a & b \\ c & d \end{pmatrix} \mid a, b, c, d \in \mathbf{F}_5, ad - bc \equiv 1 \pmod{5} \right\} \quad (5)$$

We can encode the \mathbb{B} group elements with four quinary variables $a, b, c, d = \{0, 1, 2, 3, 4\}$, and thus the matrix elements can be represented with three qubits with forbidden states $|101\rangle, |110\rangle, |111\rangle$, and the group element with 12 qubits. Alternatively, the matrix elements could be encoding into ($p = 5$) ququints without forbidden states. In this work, we derive the primitive gates of \mathbb{B} with a 12-qubit group register.

It is worth noting that the largest crystal-like subgroup of $SU(3) - \mathbb{V}$ is isomorphic to a subgroup of $GL(3, 4)$ and can be encoded with nine 9 ququarts or 18 qubits, though further investigations are needed to implement the quantum gates that can project the Hilbert space to its \mathbb{V} subspace.

III. PRIMITIVE GATE OVERVIEWS

Quantum circuits for pure gauge theories can be constructed out of a set of primitive gates [60] acting on one or more group element registers. The necessary gates for simulation and extraction of observables are:

- the inverse gate: $\mathfrak{U}_{-1}|g\rangle = |g^{-1}\rangle$, which computes in-place the inverse of a group element,
- the trace gate: $\mathfrak{U}_{\text{Tr}}(\theta)|g\rangle = e^{i\theta \text{Re Tr } g}|g\rangle$, which introduces a phase based on the trace of a group element in a particular representation and weighted by a coupling θ which can depend on the Hamiltonian and approximation used in time evolution,
- the left multiplication gate: $\mathfrak{U}_{\times}|g_i\rangle|g_j\rangle|\text{anc}\rangle = |g_i\rangle|g_j\rangle|g_i g_j\rangle$. In this work, we consider a novel definition of \mathfrak{U}_{\times} which stores the result in an ancilla group register. The right multiplication gate, if desired, can be defined via the \mathfrak{U}_{-1} , and \mathfrak{U}_{\times} ,
- the group Fourier gate: $\mathfrak{U}_F \sum_g f(g)|g\rangle = \sum_{\rho} \hat{f}(\rho)_{ij} |\rho, i, j\rangle$ with \hat{f} denoting the Fourier transform of f .

For qubit-based computers, we need 8 qubits to encode a $\mathbb{B}\mathbb{T}$ register and 12 qubits for \mathbb{B} . We will present the primitive gates construction using five entangling gates: the two qubit CNOT and SWAP, the three qubit CSWAP (known as the Fredkin gate), and the multi-controlled phase $C^n P(\phi)$ and $C^n \text{NOT}$ quantum gates. The special case of $C^2 \text{NOT}$ is commonly called the Toffoli gate. The first three implement the operations:

$$\text{CNOT } |q_1\rangle |q_2\rangle = |q_1\rangle |q_1 \oplus q_2\rangle \quad (6)$$

$$\text{SWAP } |q_1\rangle |q_2\rangle = |q_2\rangle |q_1\rangle \quad (7)$$

$$\text{CSWAP } |q_1\rangle |q_2\rangle |q_3\rangle = |q_1\rangle |q_3\rangle |q_2\rangle. \quad (8)$$

For the multi-controlled gates, which apply an operation to one qubit based on the state of $n - 1$ others we have

$$C^n P(\phi) |q_1\rangle \dots |q_n\rangle = |q_1\rangle \dots e^{i\phi q_1 \dots q_{n-1}} |q_n\rangle \quad (9)$$

$$C^n \text{NOT } |q_1\rangle \dots |q_n\rangle = |q_1\rangle \dots |q_n \oplus q_1 \dots q_{n-1}\rangle. \quad (10)$$

IV. PRIMITIVE GATES FOR $\mathbb{B}\mathbb{T}$ AND \mathbb{B}

To construct the primitive gates based on block encodings, we adopt the following conventions. We use parentheses to denote operations that return values with mod n applied for $SL(2, n)$ group. The values of matrix element are the remainders mod n . A feature of the block encoding is that the primitive gates for $\mathbb{B}\mathbb{T}$ and \mathbb{B} are quite similar, with the difference mainly arising from the modular arithmetic. Therefore we will consider the two groups in parallel as we construct their gates.

IV.1. Inverse Gate

For the construction of \mathfrak{U}_{-1} , we note that the inverse g^{-1} is given in terms of the matrix elements of g by

$$g^{-1} = \begin{pmatrix} d & -b \\ -c & a \end{pmatrix}, \quad (11)$$

TABLE II. Character Tables of (left) \mathbb{BT} and (right) \mathbb{B} including a representative element in the given conjugacy class, where $\omega = e^{2\pi i/3}$, $\tau = (1 + \sqrt{5})/2$, and $\sigma = (1 - \sqrt{5})/2$.

\mathbb{BT}	C_1	C'_1	C_4	C'_4	C''_4	C'''_4	C_6	\mathbb{B}	C_1	C'_1	C_{30}	C_{20}	C'_{20}	C_{12}	C'_{12}	C''_{12}	C'''_{12}
Ord.	1	2	3	3	6	6	4	Ord.	1	2	4	3	6	5	10	5	10
χ_1	1	1	1	1	1	1	1	χ_1	1	1	1	1	1	1	1	1	1
$\chi_{1'}$	1	1	ω	ω^2	ω	ω^2	1	χ_2	2	-2	0	-1	1	$-\tau$	τ	$-\sigma$	σ
$\chi_{1''}$	1	1	ω^2	ω	ω^2	ω	1	$\chi_{2'}$	2	-2	0	-1	1	$-\sigma$	σ	$-\tau$	τ
χ_2	2	-2	-1	-1	1	1	0	χ_3	3	3	-1	0	0	τ	τ	σ	σ
$\chi_{2'}$	2	-2	$-\omega$	$-\omega^2$	ω	ω^2	0	$\chi_{3'}$	3	3	-1	0	0	σ	σ	τ	τ
$\chi_{2''}$	2	-2	$-\omega^2$	$-\omega$	ω^2	ω	0	χ_4	4	4	0	1	1	-1	-1	-1	-1
χ_3	3	3	0	0	0	0	-1	χ_5	5	5	1	-1	-1	0	0	0	0
$ g\rangle$	$\begin{pmatrix} 1 & 0 \\ 0 & 1 \end{pmatrix}$	$\begin{pmatrix} 2 & 0 \\ 0 & 2 \end{pmatrix}$	$\begin{pmatrix} 1 & 1 \\ 0 & 1 \end{pmatrix}$	$\begin{pmatrix} 1 & 2 \\ 0 & 1 \end{pmatrix}$	$\begin{pmatrix} 2 & 2 \\ 0 & 2 \end{pmatrix}$	$\begin{pmatrix} 2 & 1 \\ 0 & 2 \end{pmatrix}$	$\begin{pmatrix} 0 & 1 \\ 2 & 0 \end{pmatrix}$	$\chi_{4'}$	4	-4	0	1	-1	-1	1	-1	1
								χ_6	6	-6	0	0	0	1	-1	1	-1
								$ g\rangle$	$\begin{pmatrix} 1 & 0 \\ 0 & 1 \end{pmatrix}$	$\begin{pmatrix} 4 & 0 \\ 0 & 4 \end{pmatrix}$	$\begin{pmatrix} 0 & 1 \\ 4 & 0 \end{pmatrix}$	$\begin{pmatrix} 0 & 1 \\ 4 & 4 \end{pmatrix}$	$\begin{pmatrix} 0 & 4 \\ 1 & 1 \end{pmatrix}$	$\begin{pmatrix} 1 & 1 \\ 0 & 1 \end{pmatrix}$	$\begin{pmatrix} 4 & 4 \\ 0 & 4 \end{pmatrix}$	$\begin{pmatrix} 1 & 2 \\ 0 & 1 \end{pmatrix}$	$\begin{pmatrix} 4 & 3 \\ 0 & 4 \end{pmatrix}$

From this, we see that the inverse operation corresponds to swapping the values of a, d and flipping the sign of b, c . Using this, the \mathfrak{U}_{-1} circuits can be derived and are presented in the left of Fig. 2 and Fig. 3 for \mathbb{BT} and \mathbb{B} respectively.

IV.2. Trace Gate

Our interest here is in the traces of each group elements in the faithful representation, which are denoted by χ_2 in Tab. II. With one representative element in the given conjugacy class in Tab. II, one can determine the other elements in the class. This enables a derivation of the rules for obtaining $\text{Tr}(g)$ in the blocking encoding. For \mathbb{BT} , these rules are

$$\text{Tr}(g) = \begin{cases} 2 & \text{if } (a+d) = 2 \ \& \ (c-b) = 0 \\ 1 & \text{if } (a+d) = 1 \ \& \ (c-b) \neq 0 \\ 0 & \text{if } (a+d) = 0 \\ -1 & \text{if } (a+d) = 2 \ \& \ (c-b) \neq 0 \\ -2 & \text{if } (a+d) = 1 \ \& \ (c-b) = 0 \end{cases} \quad (12)$$

While for \mathbb{B} , they are found to be

$$\text{Tr}(g) = \begin{cases} 0 & \text{if } (a+d) = 0 \\ 1 & \text{if } (a+d) = 1 \\ -1 & \text{if } (a+d) = 4 \\ 2 & \text{if } (a+d) = 2 \ \& \ (c^2 + b^2) = 0 \\ -\tau & \text{if } (a+d) = 2 \ \& \ (c^2 + b^2) = 1 \ \text{or } 2 \\ -\sigma & \text{if } (a+d) = 2 \ \& \ (c^2 + b^2) = 3 \ \text{or } 4 \\ -2 & \text{if } (a+d) = 3 \ \& \ (c^2 + b^2) = 0 \\ \tau & \text{if } (a+d) = 3 \ \& \ (c^2 + b^2) = 1 \ \text{or } 2 \\ \sigma & \text{if } (a+d) = 3 \ \& \ (c^2 + b^2) = 3 \ \text{or } 4 \end{cases} \quad (13)$$

By inspecting these rules, one notices that to realize $\mathfrak{U}_{\text{Tr}}(\theta)$, we first implement an addition operation

$\mathfrak{U}_a |a\rangle |b\rangle = |a\rangle |b \oplus a\rangle$. These are constructed for \mathbb{BT} and \mathbb{B} in Fig. 4. In the case of \mathbb{B} , it is useful to further define subroutines \mathfrak{U}_{+n} for $n = 1, 2$ which increment the matrix element $|a\rangle$ to $|a+n\rangle$ as shown in Fig. 5. The operation of $(b-a)$ can be implemented using \mathfrak{U}_a^\dagger . With \mathfrak{U}_a , we can construct $\mathfrak{U}_{\text{Tr}}(\theta)$, as shown in the right of Fig. 2 and Fig. 3 where θ depends on couplings and approximations in the time evolution.

IV.3. Multiplication Gate

Moving on to \mathfrak{U}_\times , the multiplication of two group elements g_i and g_j in terms of the matrix elements is

$$\begin{pmatrix} a_i & b_i \\ c_i & d_i \end{pmatrix} \begin{pmatrix} a_j & b_j \\ c_j & d_j \end{pmatrix} = \begin{pmatrix} a_i a_j + b_i c_j & a_i b_j + b_i d_j \\ c_i a_j + d_i c_j & c_i b_j + d_i d_j \end{pmatrix} \quad (14)$$

The multiplication of group elements can thus be built from blocks of \mathfrak{U}_a and a new subroutine \mathfrak{U}_p which calculate the product of two elements (See Fig. 6). In order to leave the matrix elements intact, ancillary qubits store the product. One ancillary register will be required to store each matrix element in Eq. (14). Nevertheless, to optimise the parallelization, we actually introduce one ancillary register for each product in Eq. (14), which doubles the ancillary qubits required. The group multiplication circuit \mathfrak{U}_\times is subsequently constructed as in Fig. 7.

IV.4. Fourier Gate

To reduce the quantum gates required for implementing Fourier gate, we can project both \mathbb{BT} and \mathbb{B} group to the subspace satisfying the determinate constraints using circuit \mathfrak{U}_{proj} and ancillary qubits, which will reduce the encoding from 8 and 12 qubits to 6 and 9 qubits, respectively. \mathfrak{U}_{proj} is implemented with 4 CNOT gates, 3 CSWAP gates and 8 Tofolli gates for \mathbb{BT} group. For

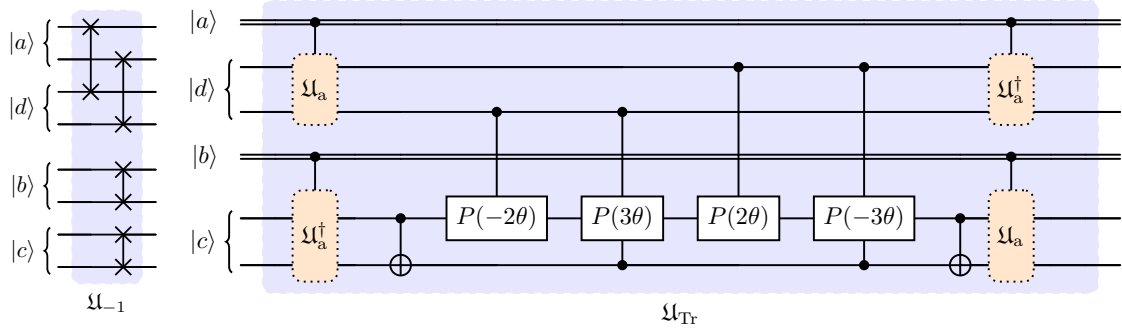


FIG. 2. Inverse gate \mathcal{U}_{-1} (left) and Trace gate $\mathcal{U}_{Tr}(\theta)$ (right) for $\mathbb{B}\mathbb{T}$ group.

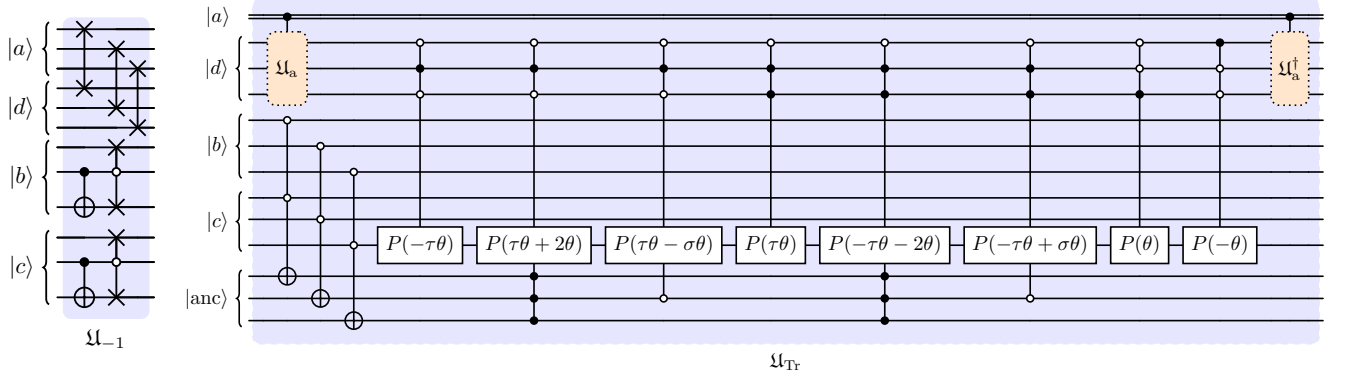


FIG. 3. Inverse gate \mathcal{U}_{-1} (left) and trace gate \mathcal{U}_{Tr} (right) for $\mathbb{B}\mathbb{B}$ group.

	\mathcal{U}_F			
	R_X	R_Y	R_Z	CNOT
$\mathbb{B}\mathbb{T}$	186	2052	3491	1941
$\mathbb{B}\mathbb{B}$	10743	131879	223044	125919

TABLE III. \mathcal{U}_F decompositions for $\mathbb{B}\mathbb{T}$ and $\mathbb{B}\mathbb{B}$ group.

the $\mathbb{B}\mathbb{B}$ group, \mathcal{U}_{proj} requires 8 CNOT gates, 3 CSWAP gates, 8 Toffoli gates and 2 \mathcal{U}_p gates. With such projection, transforming to the Fourier basis can be decomposed using the QISKIT transpiler. The resource required for Fourier gate implemented this way for both groups are shown in Tab. III.

\mathcal{U}_F is usually the most costly primitive gate for quantum simulations. Future direction should consider the fast Fourier transformation in [61] where sub-exponential growth $\exp(\sqrt{\log |\mathcal{G}|})$ of the circuit depth in the group size is possible. In particular, it should be noticed that the Fourier transformation of $\mathbb{B}\mathbb{B}$ group can be built upon that of $\mathbb{B}\mathbb{T}$ group using the following natural tower:

$$\mathbb{B}\mathbb{B} > \mathbb{B}\mathbb{T} > \mathbb{Z}_6 > \mathbb{Z}_3 > \mathbb{Z}_1 = \{1\}. \quad (15)$$

which could simplify the constructions for the $\mathbb{B}\mathbb{B}$ group once the construction for $\mathbb{B}\mathbb{T}$ is realized. It will be valuable to realize such fast Fourier transformation and compare the resources required with our brutal direct decomposition, which we will leave for the future work.

With these four primitive gates, it is possible to perform a resource estimate and compare to other implementations of $SU(2)$. This will be done in Sec. V. Additionally, the primitive gates for the two's component encoding are discussed in App. A. There we find that total gate costs are similar, but given the different distribution of qubit states, they are anticipated to have different noise robustness.

V. RESOURCE REQUIREMENTS

As T gate counts are known to require costly encodings for error correction, it is usually used in fault-tolerant resource analysis [62, 63]. We estimate the T gates required for simulating both $\mathbb{B}\mathbb{T}$ or $\mathbb{B}\mathbb{B}$ group with the block encoding method. The Toffoli gate requires 7 T gates [62]; C^n NOT can be constructed with $(2\lceil \log_2(n+1) \rceil - 1)$ toffoli gates and $n - 2$ dirty ancilla qubits for $n > 2$ [62]. C^n SWAP require the same number of T gates as C^{n+1} NOT, as it can be decomposed to C^{n+1} NOT and CNOTs using the symmetric decomposition [64]. Approximating R_Z gates to the precision of ϵ will require $1.15 \log_2(1/\epsilon)$ T gates [53], while R_X and R_Y can be implemented with at most three R_Z gates. $CP(\phi)$ requires 8 T gate and one R_Z gate with one clean ancillary qubit [65], and $C^n P(\theta)$ can be decomposed to $2(n-1)$ Toffoli gates and one $CP(\theta)$ with $n-1$ clean ancilla [66]. The

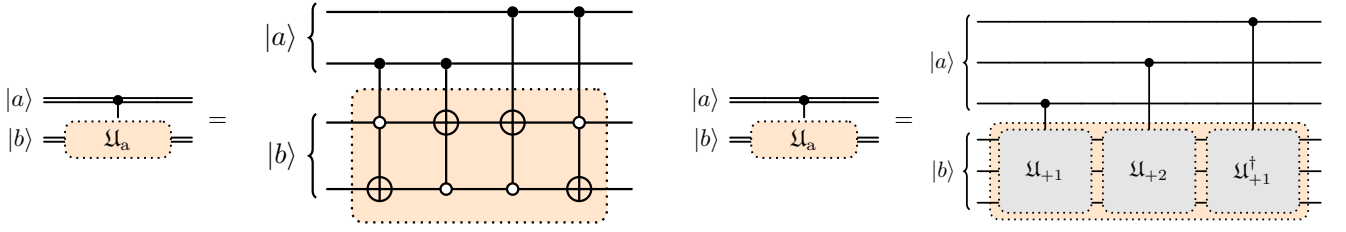


FIG. 4. Implementation of \mathfrak{U}_a for \mathbb{BT} (left) and \mathbb{B} group(right).

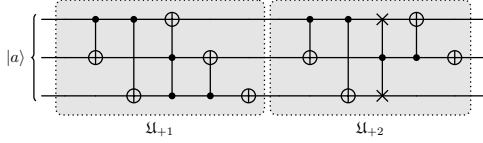


FIG. 5. Elementary operations \mathfrak{U}_{+1} and \mathfrak{U}_{+2} that add one and two to a single matrix element, respectively for the \mathbb{B} group.

gate	T (\mathbb{BT})	T (\mathbb{B})
\mathfrak{U}_{-1}	0	14
\mathfrak{U}_{T^+}	$172 + 4.6 \log_2(1/\epsilon)$	$666 + 9.2 \log_2(1/\epsilon)$
\mathfrak{U}_\times	336	3640
\mathfrak{U}_F	$11735.8 \log_2(1/\epsilon)$	$748547 \log_2(1/\epsilon)$

TABLE IV. Number of T gates required to realize the primary gates.

estimated number of T gates to realize each primary gates are listed in Tab. IV.

We compare the resources required for the calculation of the viscosity as that in [67]. The total T gate count for certain Hamiltonian H is given by $N_T^H = C_T^H \times dL^d N_t$ for a d spatial lattice simulated for a time $t = N_t \delta t$ [53], where C_T^H is the average number of T gates require per link per δt , and dL^d is the total number of links. We first consider the simulation of the \mathbb{BT} group. With the primitive gates per link per δt listed in [53], we get

$$C_T^{H_{KS}} = 2102(d-1) + (23469.2 + 2.3d) \log_2 \frac{1}{\epsilon} \quad (16)$$

$$C_T^{H_I} = 8994d - 7650 + (46936.1 + 6.9d) \log_2 \frac{1}{\epsilon}, \quad (17)$$

using the Kogut-Susskind Hamiltonian H_{KS} and the improved Hamiltonian H_I studied in [68], respectively. With this, the total synthesis error ϵ_T can be estimated as the sum of ϵ from each R_Z :

$$\epsilon_T^{H_{KS}} = 2(10204 + d)dL^d N_t \times \epsilon \quad (18)$$

$$\epsilon_T^{H_I} = 2(20407 + 3d)dL^d N_t \times \epsilon. \quad (19)$$

For simulating the \mathbb{B} , the cost increase moderately:

$$C_T^{H_{KS}} = 22215(d-1) + (1497090 + 4.6d) \log_2 \frac{1}{\epsilon} \quad (20)$$

$$C_T^{H_I} = 95793d - 81205 + (2994170 + 13.8d) \log_2 \frac{1}{\epsilon}, \quad (21)$$

and total synthesis errors:

$$\epsilon_T^{H_{KS}} = 4(325454 + d)dL^d N_t \times \epsilon \quad (22)$$

$$\epsilon_T^{H_I} = 12(216969 + d)dL^d N_t \times \epsilon. \quad (23)$$

To calculate the shear viscosity with the total synthesis error $\epsilon_T = 10^{-8}$ on a $d = 3$ lattice with $L^3 = 10^3$ for $N_t = 50$, finding $N_T^{H_{KS}} = 2.1 \times 10^{11}$ and $N_T^{H_I} = 4.2 \times 10^{11}$. For the ordered product method in simulating \mathbb{BT} group [45], the total number of T gates are estimated as 1.1×10^{11} for H_{KS} and 4.1×10^{11} for H_I . We note that H_{KS} is inadequate to reach the scaling regime with \mathbb{BT} , but for the case of H_I , the T gate cost is only $\sim 3\%$ higher – and in both cases dominated by \mathfrak{U}_F . With only 60% higher qubit costs, we conclude that this encoding is comparable to ordered product on these simple metrics, and further analysis of other metrics like noise robustness should be undertaken. For the larger \mathbb{B} group – where no ordered product encoding currently exists – the costs increase to $N_T^{H_{KS}} = 1.4 \times 10^{13}$ and $N_T^{H_I} = 2.9 \times 10^{13}$ T gates using H_{KS} and H_I , respectively. But given $\beta_f \gg \beta_s$ for \mathbb{B} , the increase in gate costs may be an acceptable trade-off due to the reduced systematic error from digitization.

We can also compare the the block encoding of discrete subgroups to other digitization methods, such as LSH formalism, in which the T gate counts has been estimated for $d = 1$ in [69]. For comparison, we take the finite arithmetic precision errors to be smaller than the synthesis error $\epsilon_T = 10^{-8}$ by taking minimally $n = 7$ steps and $m = 42$ bits to evaluate the inverse-square-root functions using Newton's method. We choose 5 and 7 qubits to encode one group register in LSH to match the minimal number of qubits that can encode \mathbb{BT} and \mathbb{B} group, respectively. With $L = 10$, LSH will require in trotter step using singular-value-decomposition roughly 3.9×10^7 and 4.2×10^7 T gates for the 5 qubits and 7 qubits case, respectively, while for the blocking encoding, \mathbb{BT} and \mathbb{B} requires 1.3×10^7 and 8.0×10^8 T gates. Given this similarity in resource costs, quantifying the systematic and statistical errors in

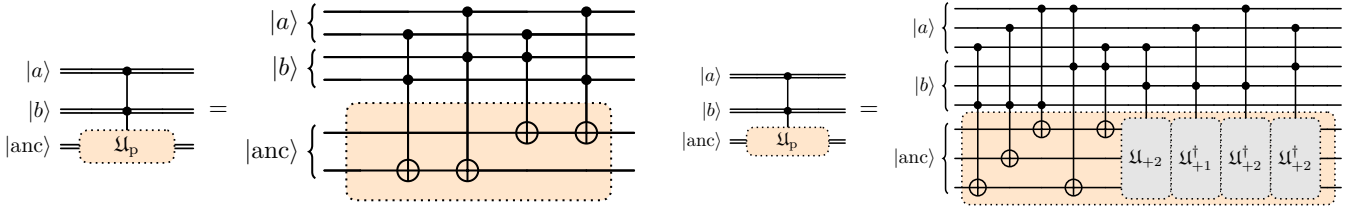


FIG. 6. Implementation of \mathcal{U}_p for $\mathbb{B}\mathbb{T}$ (left) and $\mathbb{B}\mathbb{I}$ group(right).

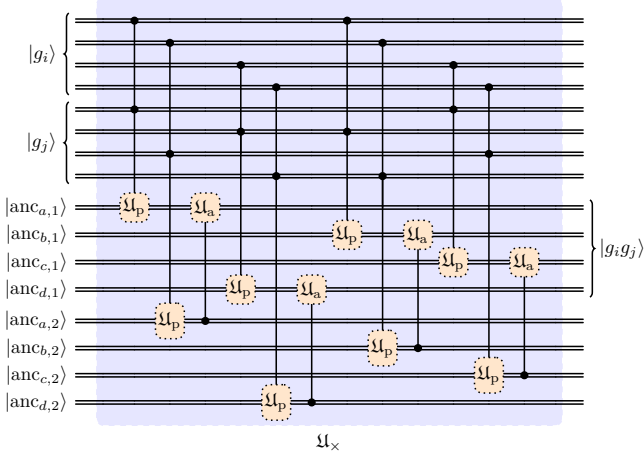


FIG. 7. Multiplication gate \mathcal{U}_x for $\mathbb{B}\mathbb{T}$ group and $\mathbb{B}\mathbb{I}$ group.

digitization methods becomes an important concern. In the next section, we consider the relative resilience of the block encoding versus the ordered product encoding. Additionally, a comprehensive comparison to other forms of approximating the time evolution in LGT [33, 70] are desirable, but understanding the theoretical LGT errors must be understood for a complete comparison.

VI. RESILIENCE TO QUANTUM ERRORS

In this section, we investigate the resilience to errors for a single register using the block encoding and the ordered product encoding, considering $\mathbb{B}\mathbb{T}$. Given that redundant degrees of freedom are introduced in both encodings, one can utilize these redundancies in QEC [54, 71–82]. In the following, we derive the error rate threshold [82] below which the block encoding would provide higher fidelity than the ordered product one for a single register as more redundancies are introduced in the block encoding. We will take the bit-flip error as an example and consider the fidelity as the one averaging over all group elements. Error mitigation using post-selection remove trials affected by detectable errors. For the error channel with only one bit-flip error \mathcal{N}_i affecting qubit i at an error rate of ϵ , we count the number of \mathcal{N}_i that transform the group element into forbidden state, which are detectable errors. Using the ordered product encoding, there are in total 16

detectable \mathcal{N}_i for all 24 group elements, while for the block encoding, 160 \mathcal{N}_i . The lower bound for the fidelity is the probability of no errors after removing trials affected by these detectable one bit-flip errors:

$$\begin{aligned} \mathcal{F}_{\text{BE}}^{\text{ps}} &\geq \frac{(1-\epsilon)^8}{1-160/24(1-\epsilon)^7\epsilon}, \\ \mathcal{F}_{\text{OPE}}^{\text{ps}} &\geq \frac{(1-\epsilon)^5}{1-16/24(1-\epsilon)^4\epsilon}. \end{aligned} \quad (24)$$

For logical error rates $\epsilon \lesssim 0.1$, we found the lower bound of the fidelity for the current encoding methods can be higher. As post-selections require resources that are exponential [83] in system size, we also consider correcting one bit-flip error to reduce the effects of quantum errors. The correctable one bit-flip errors transform the group element to a forbidden state that cannot be transformed from other group element via any one bit-flip error (Knill-Laflamme condition [84]). Under this condition, when a forbidden state is seen, the error channel can be inferred and corrected. Using the ordered product encoding, there are in total 0 correctable \mathcal{N}_i for all 24 group elements, while for the block encoding, 80 \mathcal{N}_i . Correcting trials affected by correctable \mathcal{N}_i , we obtain the lower bound of the averaged fidelity as the probability with no errors or only correctable one bit-flip errors:

$$\begin{aligned} \mathcal{F}_{\text{BE}}^{\text{cor}} &\geq (1-\epsilon)^8 + \frac{80}{24}(1-\epsilon)^7\epsilon, \\ \mathcal{F}_{\text{OPE}}^{\text{cor}} &\geq (1-\epsilon)^5. \end{aligned} \quad (25)$$

We observe that in this situation for $\epsilon \lesssim 0.1$, the lower bound of the fidelity for the current encoding methods can be mildly higher.

VII. EXPERIMENTAL RESULTS

Using the block encoding method, we benchmark the fidelity of the inverse gate for $\mathbb{B}\mathbb{T}$ and $\mathbb{B}\mathbb{I}$ group on the Quafu platform [85]. Given the availability, we used the **Baiwang** quantum real machine on Quafu which has 144 qubits and 136 available. The qubits are arranged in a 12×12 lattice, where qubits in each row are connected adjacently and adjacent rows are connected by only 2-4 qubit connections [85]. 8 qubits and 12 qubits are selected for the simulation of $\mathbb{B}\mathbb{T}$ and $\mathbb{B}\mathbb{I}$ group, respectively, with their positions and connectivity shown in Fig. 8. Starting

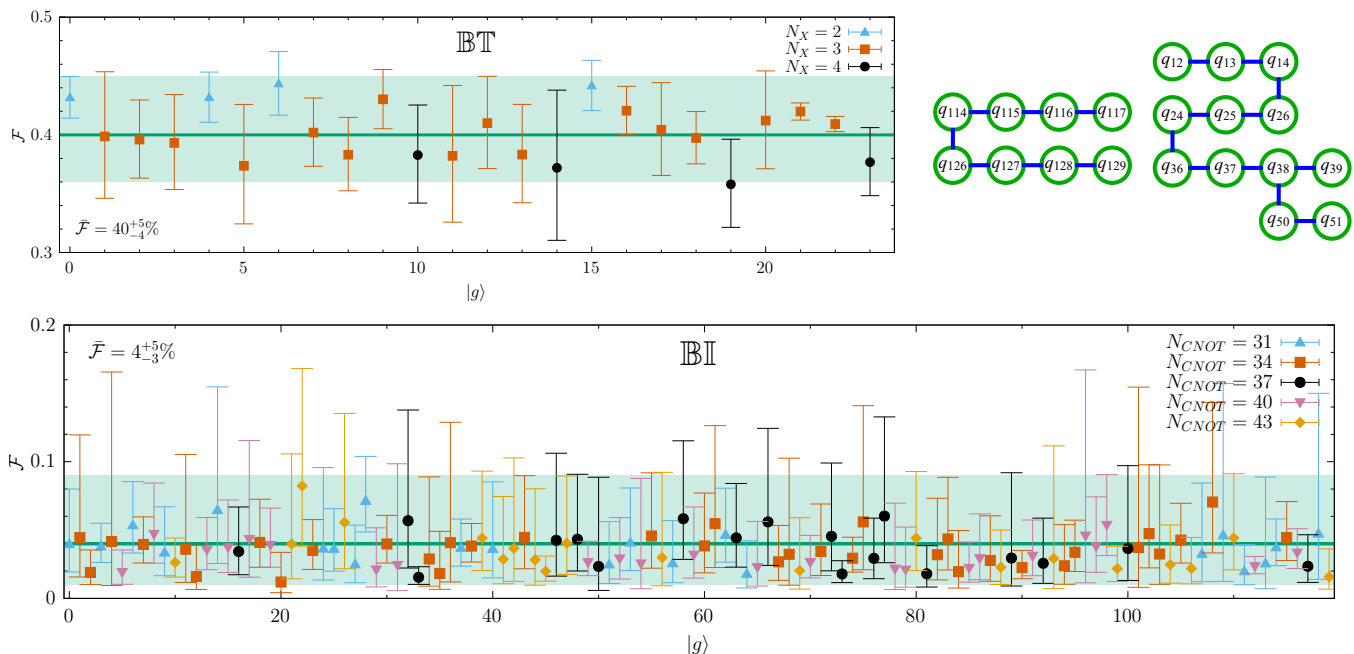


FIG. 8. Fidelity of $\mathfrak{U}_{-1}^{\text{trans}}$ gate of **Baiwang** for each group element $|g\rangle = |abcd\rangle$ of \mathbb{BT} and \mathbb{BI} , which is labeled by the lexicographic order in the range $\{0, |G| - 1\}$. The averaged fidelity $\bar{\mathcal{F}}$ over all group elements are also shown. On the top right, the qubit graph on **Baiwang** used to represent $|g\rangle$ for (left) \mathbb{BT} and (right) \mathbb{BI} respectively.

from state $|\psi_0\rangle = |0\rangle^{\otimes n_q}$ with $n_q = 8(12)$ for $\mathbb{BT}(\mathbb{BI})$ group, we can prepare the initial state $|g\rangle$ with circuit \mathfrak{U}_g : $|g\rangle = \mathfrak{U}_g|\psi_0\rangle$. For single states, \mathfrak{U}_g is simply a tensor product of X gates used to initialize some qubits to $|1\rangle$. Adapting to the connectivity of the qubits chosen on **Baiwang** quantum chip, \mathfrak{U}_{-1} requiring 12 (29) CNOT gates¹ is transpiled to $\mathfrak{U}_{-1}^{\text{trans}}$ with 18 (31-43) CNOTs for $\mathbb{BT}(\mathbb{BI})$ where the number of CNOTs is counted from the transpiled circuits. We subsequently apply $\mathfrak{U}_{-1}^{\text{trans}}$ to $|g\rangle$, resulting in the final state $|g'\rangle$. The fidelity is defined as:

$$\mathcal{F} = |\langle g^{-1}|g'\rangle|^2 = |\langle g^{-1}|\mathfrak{U}_{-1}^{\text{trans}}\mathfrak{U}_g|\psi_0\rangle|^2, \quad (26)$$

which is the probability of measuring the correct $|g^{-1}\rangle$.

We show the fidelity for each g in Fig. 8 for both \mathbb{BT} and \mathbb{BI} group calculated from 5 runs, with $N = 5000$ shots for each run. The group element are labeled by the lexicographic order in the range $\{0, |G| - 1\}$ on the x-axis. We also present the average fidelity $\bar{\mathcal{F}}$ over all group elements in Fig. 8. The fidelity of an X gate is found to be $\sim 97\%$ on **Baiwang**, and CNOT gate to be around $\sim 95\%$. For the \mathbb{BT} case, different X gates are needed to prepare state, which are denoted with different shapes in the upper plot of Fig. 8. This variance in state preparation causes noticeable variations in \mathcal{F} observed in Fig. 8.

We found that for $|g\rangle$ in \mathbb{BT} , the error rate grows slightly with number of X gates in $\mathfrak{U}_{-1}^{\text{trans}}$. We can achieve $\bar{\mathcal{F}} =$

$40\%_{-4}^{+5}$ for \mathbb{BT} . Previously, the fidelity of a 5-qubit ordered product encoding \mathfrak{U}_{-1} was benchmarked on **ibm_nairobi** with higher connectivity and Pauli twirling used for error mitigation [45]. Despite these striking differences, a value of $\mathcal{F} = 37.0\%_{-8}^{+8}$ was found, suggesting that effective digitizations depend on connectivity and noise channels in nontrivial ways, and therefore should not be neglecting in the discussion of digitization.

For \mathbb{BI} , $\mathfrak{U}_{-1}^{\text{trans}}$ can require varying numbers of CNOTs depending on the transpilation, including the orders of mapping matrix elements to the qubits. We show the fidelities for each group element of \mathbb{BI} in Fig. 8 (bottom) with shapes indicating the number of CNOTs in the transpiled circuits. As more than 30 CNOT gates are involved, noises from CNOT gates contribute mostly to the quantum errors which reduce the average fidelity to only $\bar{\mathcal{F}} = 4\%$. This low fidelity could be improved by improving transpilation that reduces CNOT counts and implementing error mitigation strategies such as Pauli twirling which have proven effective in the past for LGT [45, 68, 86–90].

VIII. CONCLUSIONS AND OUTLOOK

In this work, we introduced the block encoding method – a general method for digitizing discrete groups on quantum computers and developed the primitive gate set for the two important discrete subgroup of $SU(2)$, including the first implementation ever of \mathbb{BI} . The realization of quantum circuits for this largest crystal-like subgroup of $SU(2)$ allows simulating physics of $SU(2)$ deep into the

¹ We have decomposed the Fredkin gate to 7 CNOTs [64]

scaling regime. We have shown that the qubit and T costs as well as robustness to noise are comparable to other digitizations of $SU(2)$. In particular both theoretical analysis and experimental results support the idea that the connection between number of qubits/gates and the true performance in a noisy environment is nontrivial, and such metrics should be considered in evaluating digitization schemes.

A number of directions for research exist following these results. Primary is that given its predominance in the gate costs, determination of a quantum Fourier transformation gate within the block encoding method would be invaluable, as it could radically improve the resources costs. Further, given the block encoding method separates group registers into registers of finite fields, it may greatly benefit from formulations on qudit-based devices similar to [9, 55, 91–98]. Finally, the outstanding goal of the discrete group approximation is the largest crystal-like subgroup of $SU(3) - \mathbb{V} -$ which similar to \mathbb{B} faces obstacles in the order product encoding. Given it is isomorphic to a subgroup of $GL(3, 4)$, the block encoding method provides an avenue for encoding it onto nine 9 ququarts or 18 ququits.

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Appendix A: Primitive Gates for $\mathbb{B}\mathbb{T}$ Group with Two's Component Encoding

In this section, we present the primitive gates for $\mathbb{B}\mathbb{T}$ group with two's component encoding, e.g. $|a=0\rangle = |00\rangle$, $|a=1\rangle = |01\rangle$ and $|a=-1\rangle = |11\rangle$. Given the addition and multiplication table below, the quantum circuit to implement matrix element additions \mathcal{U}_a and productions \mathcal{U}_p are shown in Fig. 9. The construction of the group multiplication circuit \mathcal{U}_x is similar as Fig. 7 in the main text. The inverse and trace operations follow the same rules as Eq. (11) and Eq. (12) in the main text, but with different quantum circuits given in Fig. 10.

TABLE V. Addition.

	00	01	11
00	00	01	11
01	01	11	00
11	11	00	01

TABLE VI. Multiplication.

	00	01	11
00	00	00	00
01	00	01	11
11	00	11	10

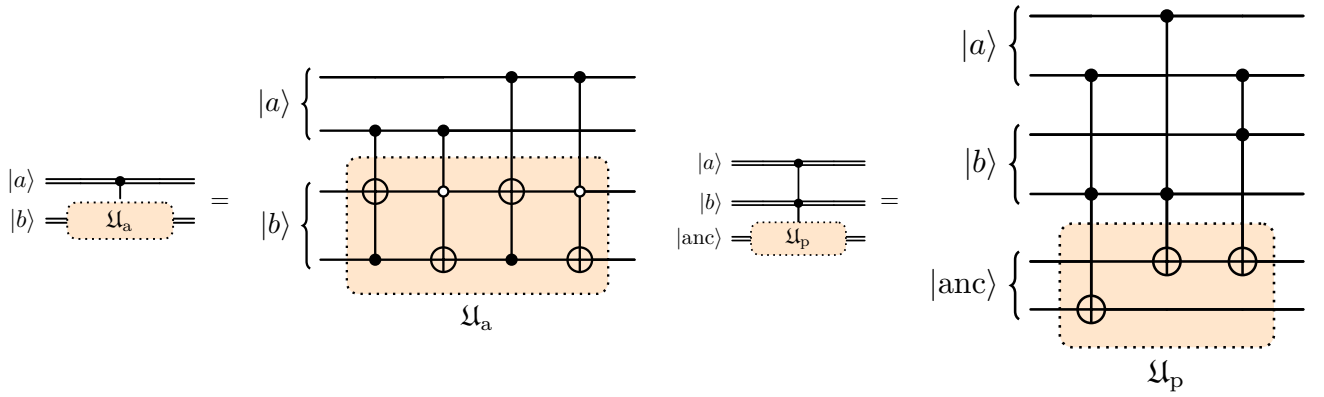


FIG. 9. Quantum circuit implementing matrix element additions and productions in \mathbb{F}_5 .

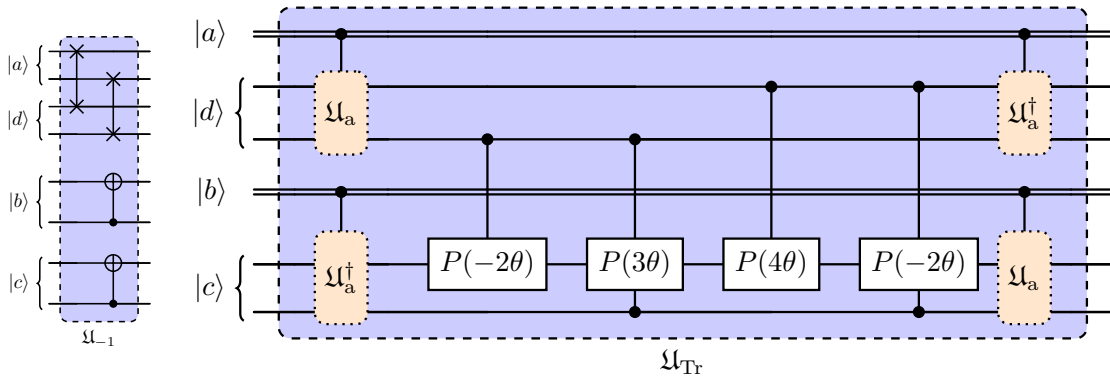


FIG. 10. Inverse gate \mathcal{U}_{-1} and trace gate \mathcal{U}_{Tr} for $\mathbb{B}\mathbb{T}$ group with the two's component encoding.