GENERAL PHYSICAL LAYOUT:

- There are a total of 9 slots, designated 1 through 9, from left to right.
- Each slot is associated with a 96 pin Class 2 DIN 41612 (VME type) connector.
- All even numbered slots are right handed (Connectors to the right of the card), as would be found on a normal VME J1 backplane. These slots utilize standard DIN connectors.
- All odd numbered slots are left handed. These slots utilize standard 96 pin Class 2 DIN 41612 connectors orientated with 180 degrees of rotation. Thus, the connector's A1 position is associated with the lower right pad of the backplane for that particular connector. Likewise, the connector's C32 position is associated with the upper left pad of the backplane for that particular connector.
- 5 layers (comp, gnd, pwr, gnd, sold)
- .125" thick.
- Mechanical layout presented in drawing number 3823.113-MD-330044.

ELECTRICAL CHARACTERISTICS:

- The backplane is routed as though it were a standard 9 slot VME J1 backplane, with J1 as the master slot, as defined in ANSI/IEEE 1014-1987 standard.
- The presence of odd slots has no effect on backplane routing. At each and every slot, A1 and the signal assigned A1 by the VME standard is located in the upper left hand corner as viewed from the front of the backplane. Thus, the routing paths are the same as a standard VME J1 backplane.
- Line impedances, terminations, bus grant and interrupt acknowledge jumper pins shall be the same as a standard VME J1 backplane, as defined in the ANSI/IEEE 1014-1987 standard.
- The space between DIN connectors on the front of the backplane shall be kept clear. Any bus grant and interrupt acknowledge jumper pins shall be located on the rear of the backplane.

DC POWER:

- Each slot is supplied with +5 and +12 volts, and defined in the ANSI/IEEE 1014-1987 standard.
- +5V, +12V, and GND originates at rear of the backplane via press-in power terminals. Their locations are shown in drawing number 3823.113-MD-330044.
- Power shall be bypassed at 3 locations with a 10uF tantalum capacitor in parallel with a .01uF 100V ceramic capacitor.